09/489681

PACENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Branko KOVACEVIC et al.

Assignee:

ATI Technologies, Inc.

Title:

METHOD FOR DISPLAYING DATA

Patent No.:

6,999,424

Issued:

February 14, 2006

Atty. Docket No.:

1376-9901420

MS: Certificate of Correction Branch COMMISSIONER FOR PATENTS PO Box 1450 Alexandria, VA 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT— PTO MISTAKE (37 C.F.R. § 1.322(a))

Dear Sir:

Pursuant to 35 U.S.C. § 254 and 37 C.F.R. § 1.322(a), please issue a Certificate of Correction in the above-identified matter. The mistake(s) to be corrected was/were made by the U.S. Patent Office.

- 1. Attached hereto, in duplicate, is Form PTO-1050, with at least one copy suitable for printing.
- 2. The exact page(s) and line number(s) where the error(s) is/are shown correctly in the application file:

Amendment After Allowance dated October 20, 2005, page 2 of 8, Claim 1; page 3 of 8, Claim 8 (renumbered as Claim 6); page 4 of 8, Claim 16 (renumbered as Claim 13); page 5 of 8, Claim 23 (renumbered as Claim 20); page 6 of 8, Claim 33 (renumbered as Claim 29).

3. Please send the Certificate to:

J. Gustav Larson LARSON NEWMAN ABEL POLANSKY & WHITE, L.L.P. 5914 West Courtyard Drive, Suite 200 Austin, Texas 78730

Respectfully submitted,

6-16-02

Date

J. Gustav Larson, Reg. No. 39,263

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JUN 2 6 ZUUG

JUN Certificate

Of Correction

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO

: 6,999,424

DATED

: February 14, 2006

INVENTOR(S): Branko KOVACEVIC et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Specification:

Column 29, line 43, please change "splice countdown O" to "splice countdown = O"

In the Claims:

Column 35, line 43, please change "hardware." to "hardware,"

Column 35, line 63, please change "claim 1" to "claim 5"

Column 36, line 42, please change "tort" to "transport"

Column 37, line 10, please change "her" to "further"

Column 38, line 5, please change "fluffier" to "further"

MAILING ADDRESS OF SENDER:

LARSON NEWMAN ABEL POLANSKY & WHITE, L.L.P. 5914 West Courtyard Drive, Suite 200 Austin, TX 78730

PATENT NO. 6,999,424

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO

: 6,999,424

DATED

: February 14, 2006

INVENTOR(S): Branko KOVACEVIC et al.

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MAILING ADDRESS OF SENDER:

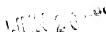
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Branko KOVACEVIC et al.

Title:

METHOD FOR DISPLAYING DATA

App. No.:

09/489,681

Filed:

January 24, 2000

Examiner:

Man U. PHAN

Group Art Unit:

2665

Customer No.: 34456

Confirmation No.:

6134

Atty. Dkt. No.: 0100.9901420

(1376-9901420)

Mail Stop ISSUE FEE Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

AMENDMENT AFTER ALLOWANCE (37 C.F.R. §1.312)

Dear Sir:

This Amendment is being submitted following the Notice of Allowance mailed on July 20, 2005 and prior to payment of the issue fee.

Claim Amendments begin on page 2.

Remarks begin on page 8.

CERTIFICATE OF TRANSMISSION/MAILING

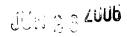
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient first class mail, in an envelope addressed to the Commissioner for Patents on

Typed or Printed Name

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IN THE CLAIMS:

- 1. (Currently Amended) A method comprising:
- detecting a first splice indicator using transport packet demultiplexer hardware, wherein detecting the first splice indicator includes generating a first splice interrupt based upon the first splice indicator;
- determining a new packet identifier when, in response to detecting the first splice indicator, it is determined that a first splice state has been encountered, wherein the first splice state is based upon a first splice countdown value parsed by the transport packet demultiplexer hardware, wherein the new packet identifier is determined in response to the first splice interrupt;
- wherein detecting the second splice indicator includes generating a second splice interrupt based upon the second splice indicator; and using the new packet identifier in response to the second splice interrupt.
- (Previously Presented) The method of claim 1 further comprising:
 loading the new packet identifier into a shadow register after determining the new packet identifier and before using the new packet identifier.
- 3. (Previously Presented) The method of claim 2, wherein using the new packet identifier further comprises loading the contents of the shadow register into a main register.
- 4. (Previously Presented) The method of claim 2, wherein using the new packet identifier further comprises using the shadow register as the main register.
- 5. (Previously Presented) The method of claim 1, wherein detecting the first splice indicator includes using an adaptation field parser portion of the transport packet demultiplexer hardware.
 - 6. (Canceled)
 - 7. (Canceled)



- 8. (Original) The method of claim wherein the first splice indicator and the second splice indicator represent different occurrences of a common event.
- 9. (Original) The method of claim 8, wherein the common event is the assertion of a splice flag.
 - 10. (Canceled)
- 11. (Previously Presented) The method of claim 1, wherein determining further includes the first splice countdown value being a positive value.
 - 12. (Previously Presented) The method of claim 1 further including: using the new packet identifier in response to the second splice interrupt, when, in response to detecting the second splice indicator it is determined that a second splice state has been encountered, wherein the second splice state is based upon a second splice countdown value parsed by the transport packet demultiplexer hardware.
- 13. (Previously Presented) The method of claim 12, wherein using further includes the second splice countdown value being a zero value.
- 14. (Previously Presented) The method of claim 13, wherein determining further includes the first splice countdown value being a positive value.
 - 15. (Previously Presented) The method of claim 1, further comprising: detecting a third splice indicator using transport packet demultiplexer hardware; requesting acquisition of a current program management table in response to the third splice indicator.

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16. (Currently Amended) A method comprising:

detecting a first splice indicator using transport packet demultiplexer hardware;

determining a new packet identifier when, in response to detecting the first splice

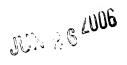
indicator, it is determined that a first splice state has been encountered, wherein

the first splice state is based upon a first splice countdown value parsed by the

transport packet demultiplexer hardware;

detecting a second splice indicator using the transport packet demultiplexer hardware; using the new packet identifier in response to the second splice indicator;[[-]] detecting a third splice indicator using transport packet demultiplexer hardware; and requesting acquisition of a current program management table when, in response to detecting the third splice indicator, it is determined that a third splice state has been encountered, wherein the third splice state is based upon a third splice countdown value parsed by the transport packet demultiplexer hardware.

- 17. (Previously Presented) The method of claim 16, wherein determining further includes the first splice countdown value being a negative value.
- 18. (Previously Presented) The method of claim 16, further comprising verifying the new packet identifier.
 - 19. (Currently Amended) The method of claim 1, wherein:
 using the new packet identifier in response to the second splice interrupt further includes
 using the new packet identifier in response to the second splice interrupt when the
 new packet identifier is associated with a first program type.
- 20. (Currently Amended) The method of claim 19, wherein the first program type is mutually exclusive from a second program type, and the second program type is a commercial [[s]] type.



21. (Previously Presented) A method comprising:

detecting a first splice indicator using transport packet demultiplexer hardware, wherein detecting the first splice indicator includes generating a first splice interrupt; determining a new packet identifier in response to the first splice interrupt; detecting a second splice indicator using the transport packet demultiplexer hardware; using the new packet identifier in response to the second splice indicator; detecting a third splice indicator using transport packet demultiplexer hardware; and requesting acquisition of a current program management table in response to the third splice indicator.

- 22. (Previously Presented) The method of claim 21 further comprising:
 loading the new packet identifier into a shadow register after determining the new packet identifier and before using the new packet identifier.
- 23. (Previously Presented) The method of claim 22, wherein using the new packet identifier further comprises loading the contents of the shadow register into a main register.
- 24. (Previously Presented) The method of claim 22, wherein using the new packet identifier further comprises using the shadow register as the main register.
- 25. (Previously Presented) The method of claim 21, wherein detecting the first splice indicator includes using an adaptation field parser portion of the transport packet demultiplexer hardware.
 - 26. (Canceled)
 - 27. (Previously Presented) The method of claim 21, wherein detecting the second splice indicator includes generating a second splice interrupt based upon the second splice indicator; and using the new packet identifier occurs in response to the second splice interrupt.

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- 28. (Previously Presented) The method of claim 27, wherein the first splice indicator and the second splice indicator represent different occurrences of a common event.
- 29. (Previously Presented) The method of claim 28, wherein the common event is the assertion of a splice flag.
 - 30. (Previously Presented) A method comprising:

 detecting a first splice indicator using transport packet demultiplexer hardware;

 determining a new packet identifier in response to the first splice indicator;

 detecting a second splice indicator using the transport packet demultiplexer hardware;

 using the new packet identifier in response to the second splice indicator;

 detecting a third splice indicator using transport packet demultiplexer hardware; and

 requesting acquisition of a current program management table when, in response to

 detecting the third splice indicator, it is determined that a third splice state has

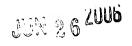
 been encountered, wherein the third splice state is based upon a third splice

 countdown value parsed by the transport packet demultiplexer hardware.
- 31. (Previously Presented) The method of claim 30, wherein determining further includes the third splice countdown value being a negative value.
- 32. (Previously Presented) The method of claim 30, further comprising verifying the new packet identifier.
 - 33. (Currently Amended) The method of claim 30, wherein:
 using the new packet identifier in response to the second splice indicator further includes
 using the new packet identifier in response to the second splice indicator when the
 new packet identifier is associated with a first program type.
- 34. (Previously Presented) The method of claim 33, wherein the first program type is mutually exclusive from a second program type, and the second program type is a commercial type.

JUN 26 2006

- 35. (Previously Presented) A system comprising:
- a means for detecting a first splice indicator using transport packet demultiplexer hardware, wherein the means for detecting the first splice indicator includes a means for generating a first splice interrupt in response to the first splice indicator;
- a means for determining a new packet identifier when, in response to detecting the first splice indicator it is determined that a first splice state has been encountered, wherein the first splice state is based upon a first splice countdown value parsed by the transport packet demultiplexer hardware, wherein the new packet identifier is determined in response to the first splice interrupt;
- a means for detecting a second splice indicator using the transport packet demultiplexer hardware, wherein the means for detecting the second splice indicator includes a means for generating a second splice interrupt in response to the first splice indicator; and
- a means for using the new packet identifier in response to the second splice interrupt.
- 36. (Previously Presented) The system of claim 35, further comprising:a means for loading the new packet identifier into a shadow register after determining the new packet identifier and before using the new packet identifier.
- 37. (Canceled)
- 38. (Canceled)
- 39. (Currently Amended) A method comprising:

detecting a first splice indicator using transport packet demultiplexer hardware; and requesting acquisition of a current program management table when, in response to detecting the first splice indicator it is determined that a first splice state has been encountered, wherein the first splice state is based upon a first splice countdown value parsed by the transport packet demultiplexer hardware.



REMARKS

Claims 1, 16, 19, 20, 33 and 39 have been amended to correct various informalities. The amendments to the claims do not change the scope of the claims. Entry thereof is therefore respectfully requested.

Should the Examiner deem that any further action by the Applicant would be desirable for placing this application in even better condition for issue, a call to the Applicant's representative listed below is requested.

The Commissioner is hereby authorized to charge any fees that may be required, or credit any overpayment, to Deposit Account Number 50-0441.

Respectfully submitted,

20 October 2005

Date

Ryan S. Davidson, Reg. No. 51,596 TOLER, LARSON & ABEL, L.L.P. 5000 Plaza On The Lake, Suite 265 Austin, Texas 78746

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